

**Appl. No. 10/650,278
Amdt. dated November 18, 2005
Reply to Office action of October 24, 2005**

Amendments to the Specification:

Please replace paragraph [0023] with the following amended paragraph:

[0023] Fig. 3 shows an example of an integrated memory device 300. Device 300 may be an assisted memory device such as that disclosed in related U.S. Patent Application No. 10/384,053, filed March 6, 2003, _____ (Attorney Docket No. 200208167), which is hereby incorporated herein by reference. An assisted memory device is a memory device that incorporates error correction techniques to cope with potential errors in storage or recovery of data. Such errors might be caused by (among other things) isolated manufacturing faults in memory cells, unpredictable inter-cell coupling faults, or transient signalling faults in the row and column lines during read and write operations.